

Please amend the claims as follows:

sub B1
11

1. (Amended) A microcomputer having a built-in nonvolatile memory including:
a communication circuit for receiving a test program for a nonvolatile memory from an external check system;
a RAM on which said test program is run; and
a boot ROM in which a control program for enabling receiving of said test program through said communication circuit and running of said test program on said RAM.

sub B1
11

4. (Amended) A check system of a microcomputer having a built-in nonvolatile memory furnished with:
at least one external communication device connected to said microcomputer in such a manner so as to allow a communication in a one-to-one correspondence,
each external communication device including,
a storage device having stored a test program for a built-in nonvolatile memory in said microcomputer, and
a communication microcomputer for sending said test program to said microcomputer,
wherein said microcomputer includes a boot ROM in which a control program for enabling receiving of said test program through a communication circuit and running of the test program on a RAM.

Sub B1
#3
6. (Amended) A check system of a microcomputer having a built-in nonvolatile memory furnished with an external communication device including:

a storage device having stored a test program for said microcomputer having a built-in nonvolatile memory;

a communication control circuit for controlling a communication with said microcomputer; and

a communication microcomputer for sending said test program to said microcomputer when checking the built-in nonvolatile memory therein,

wherein said microcomputer includes a boot ROM in which a control program for enabling receiving of said test program through a communication circuit and running of the test program on a RAM.

Sub B1
#4
8. (Amended) An IC card packing a microcomputer having a built-in nonvolatile memory including:

a communication circuit for receiving a test program for a nonvolatile memory from an external check system;

a RAM on which said test program is run, and

a boot ROM in which a control program for enabling receiving of said test program through said communication circuit and running of said test program on said RAM.

Sub B1
#5
11. (Amended) A check system of an IC card packing a microcomputer having a built-in nonvolatile memory furnished with:

Sub B1
A5

at least one external communication device connected to said microcomputer packed in said IC card in such a manner so as to allow a communication in a one-to-one correspondence, each external communication device including, a storage device having stored a test program for a built-in nonvolatile memory in said microcomputer, and a communication microcomputer for sending said test program to said IC card, wherein said microcomputer includes a boot ROM in which a control program for enabling receiving of said test program through a communication circuit and running of the test program on a RAM.

Sub B1
A6

13. (Amended) A check system of an IC card packing a microcomputer having a built-in nonvolatile memory furnished with an external communication device including: a storage device having stored a test program for a built-in nonvolatile memory in said microcomputer packed in said IC card; a communication control circuit for controlling a communication with said IC card; and a communication microcomputer for sending said test program to said IC card when checking said built-in nonvolatile memory, wherein said microcomputer includes a boot ROM in which a control program for enabling receiving of said test program through a communication circuit and running of the test program on a RAM.
